

FIG. 1

FIG. 2 is a block diagram of a system 36. The system 36 includes a control system 38, a removable memory 54, a telephony interface 52, an auxiliary interface 56, an audio processor 46, a speaker(s) 48, a microphone(s) 50, and a memory 40. The memory 40 includes software 42 and data 44.

36

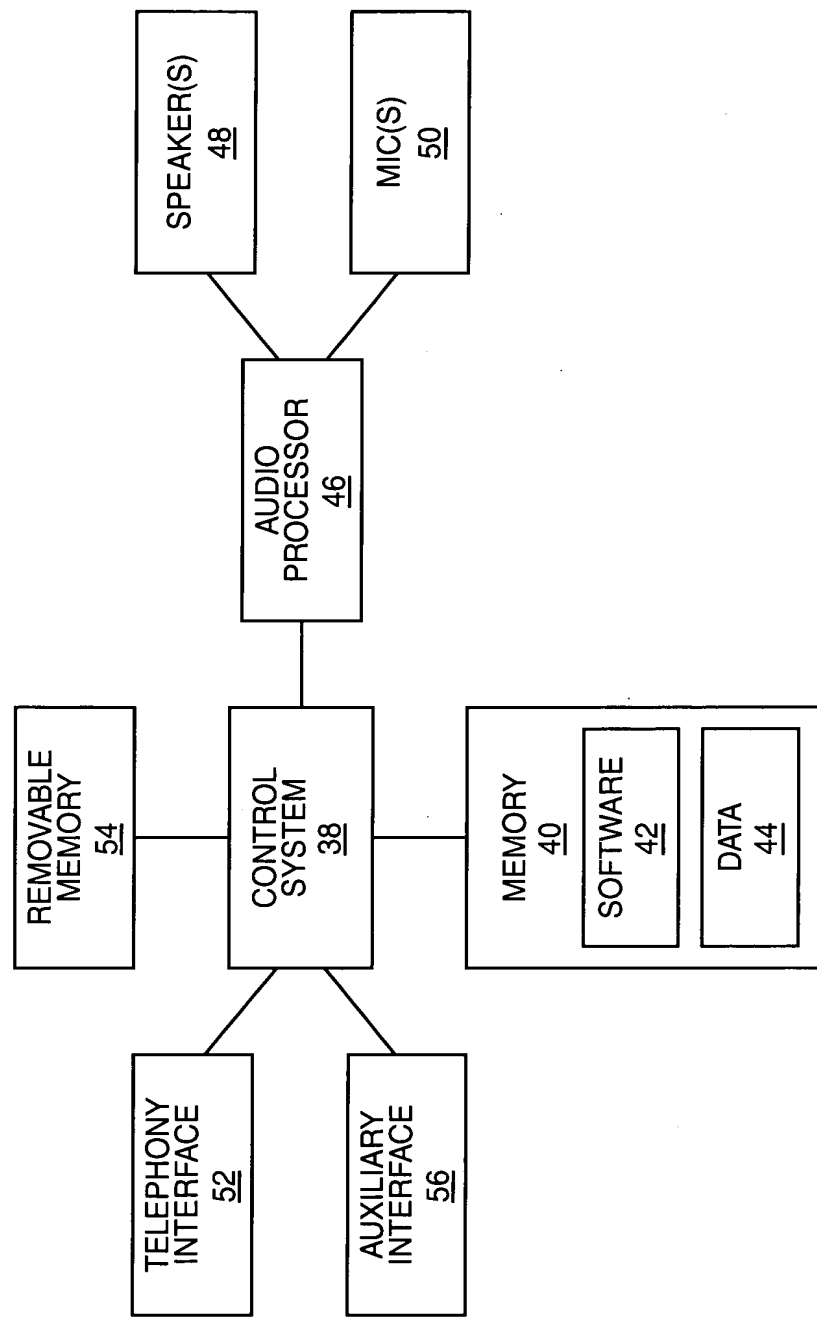


FIG. 2

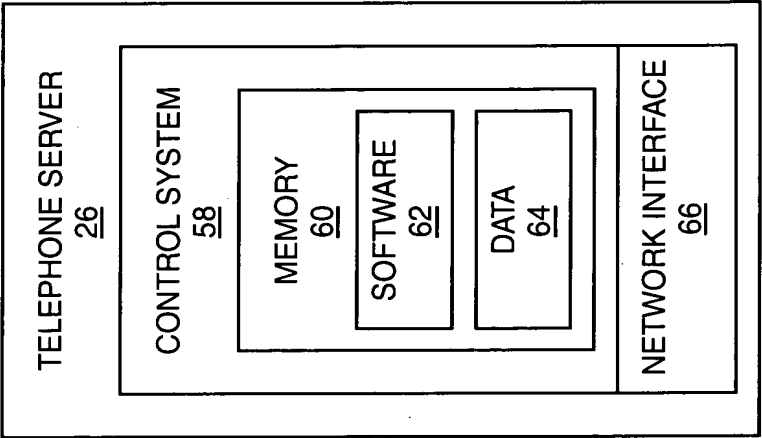


FIG. 3

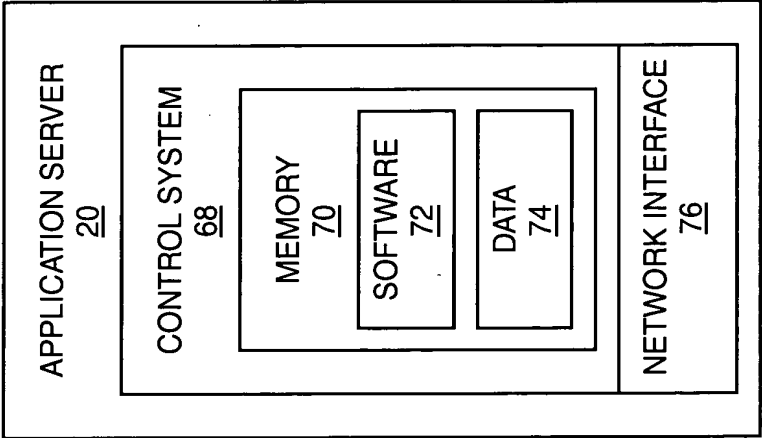


FIG. 4

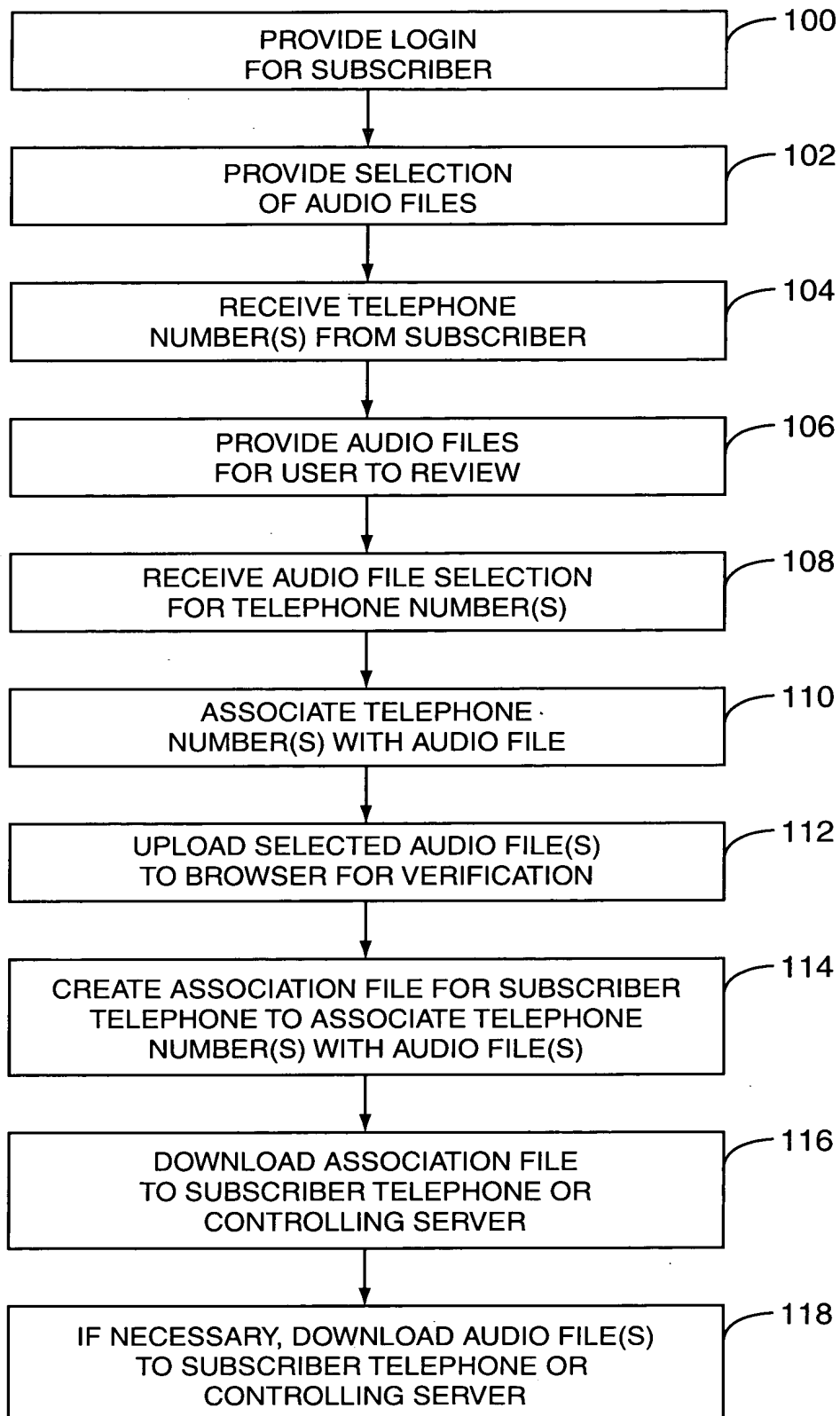


FIG. 5

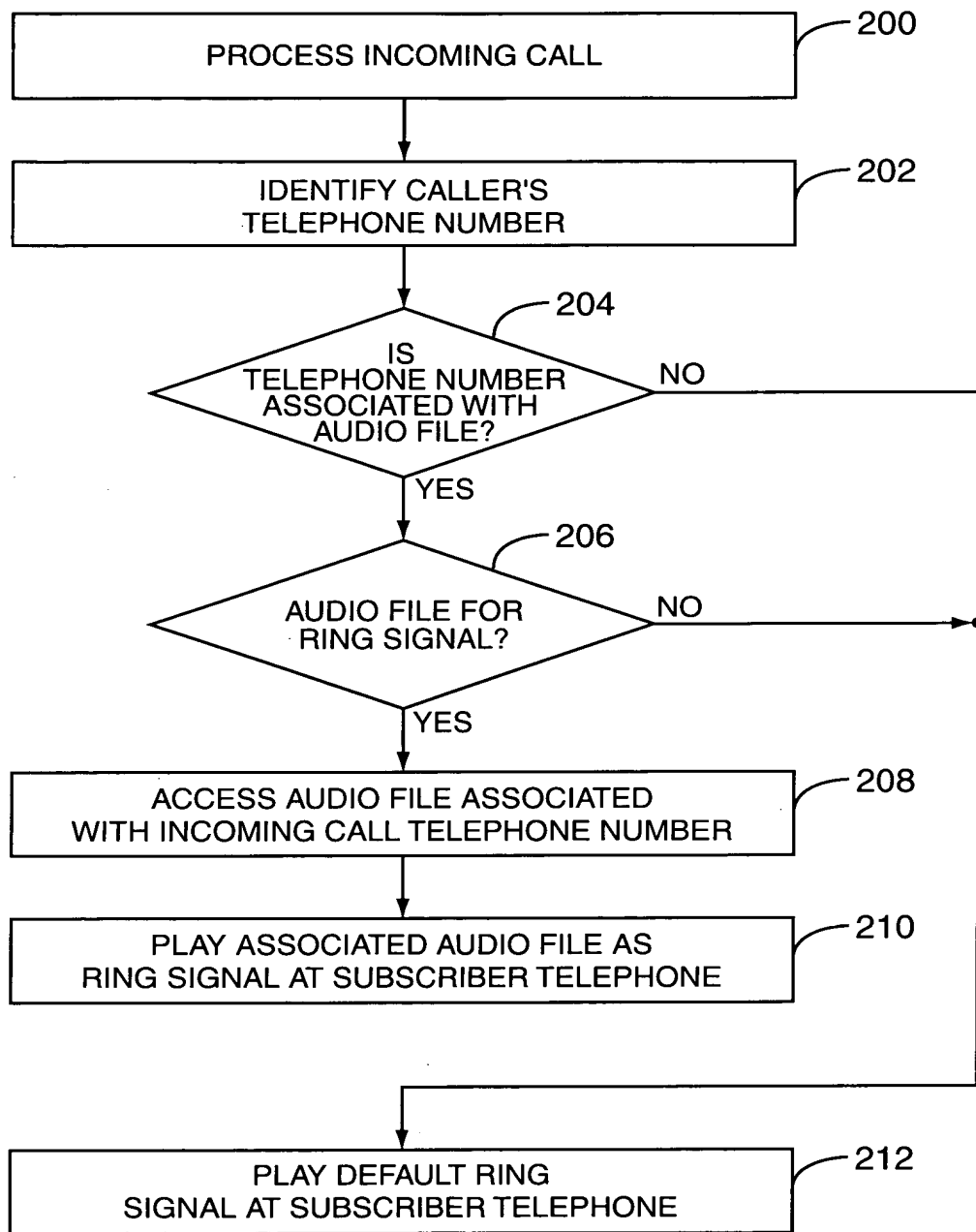


FIG. 6